

First Named Inventor	Kevin C. Widmer	<p style="text-align: center;">INFORMATION DISCLOSURE STATEMENT</p>
Serial No.	Unknown	
Filing Date	Filed herewith	
Group Art Unit	Unknown	
Examiner Name	Unknown	
Attorney Docket No.	400.170US02	
Title: DDR SYNCHRONOUS FLASH MEMORY WITH VIRTUAL SEGMENT ARCHITECTURE		

Mail Stop: PATENT APPLICATION
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

In compliance with 37 C.F.R. §§ 1.56 and 1.97, *et seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified Application. Applicant respectfully requests that this Information Disclosure Statement be entered and the references listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to MPEP §609, Applicant further requests that the Examiner initial next to each reference on the Form 1449 to indicate that the listed references have been considered. Applicant further requests that a copy of the initialed Form 1449 be returned with the next official communication.

Further pursuant to MPEP §609, because all of the references listed on the attached Form 1449 have been previously submitted and made of record in the parent application, U.S. Patent Application 10/102,757, filed March 21, 2002, copies of the references previously made of record in the parent application are not submitted herewith.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at the number listed below.

Date: 9/23/03

Respectfully submitted,



Andrew C. Walseth
Reg. No. 43,234

Attorneys for Applicant
Leffert Jay & Polglaze, P.A.
P.O. Box 581009
Minneapolis, MN 55458-1009

T – 612/312-2200
F – 612/312-2250

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Title: DDR SYNCHRONOUS FLASH MEMORY WITH VIRTUAL SEGMENT ARCHITECTURE		Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Document No.	Publication Date (mm/dd/yy)	Name	Filing Date (mm/dd/yy)
	5,646,893	07/08/97	McMinn et al.	09/07/95
	6,157,560	12/05/00	Zheng	01/25/99
	6,172,893	01/01	Ryan	
	6,236,618 B1	05/22/01	Roy	04/03/00
	6,335,874 B1	01/01/02	Eitan	09/11/00
	6,396,764 B1	05/28/02	Holland	11/16/00
	6,442,076	08/02		

Foreign Patent References					
Examiner Initials	Foreign Patent		Name	Publication Date	Translation
	Country	No.			

Other References	
Examiner Initials	Author, Title, Date, Pages, etc.

Examiner Signature		Date Considered	
<p>*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</p>			